

IN THE CLAIMS

Please amend the claims as follows:

1-7. (Cancelled).

8. (Currently Amended) An information carrier as claimed in
claim 7 comprising a recording area for writing patterns which
represent user information and a header area comprising patterns
which represent header information, said header area comprising a
5 synchronization area comprising a predetermined synchronization
pattern for synchronizing a clock frequency in a device in which
the information carrier is used, characterized in that the
predetermined synchronization pattern comprises a first part and a
second part, the second part being distinguishable from the first
10 part, characterized in that the predetermined synchronization
pattern is composed of marks and of spaces between the marks, and
in that the first part of the predetermined synchronization pattern
contains marks having a first length and spaces having a second
length whereas the second part of the synchronization pattern
15 contains marks having a third length and spaces having a fourth
length, the first length being different from the third length and
the second length being different from the fourth length, in that
the header information is converted into patterns in the header
area according to a (d,k) Run Length Limited modulation code, in

20 which d represents a predetermined natural number larger than zero
and k represents a predetermined natural number larger than d, and
the length of each mark and each space expressed as a number of
channel bit lengths (T), and in that the first part of the
predetermined synchronization pattern contains marks having a first
25 length of (d+1) times the channel bit length, and spaces having a
second length of (d+1) times the channel bit length, and the second
part of the predetermined synchronization pattern contains marks
having a third length of (k+1) times the channel bit length and
spaces having a fourth length of (k+1) times the channel bit
30 length, and in that the predetermined synchronization pattern also
comprises a third part, which third part contains marks having a
length of (k-d) times the channel bit length and spaces also having
a length of (k-d) times the channel bit length.

9-10 (Cancelled) .

11. (Currently Amended) A reading device for reproducing
information from an information carrier ~~which comprises a~~
~~predetermined synchronization patterns as claimed in claim 8, the~~
reading device comprising reading means for reading the
5 predetermined synchronization pattern and synchronization means for
setting a clock frequency and for setting a dynamic range of an
amplifier in response to the predetermined synchronization pattern

read, characterized in that the synchronization means comprise means for setting the clock frequency and for setting the dynamic 10 range of an amplifier in response to the predetermined synchronization pattern ~~according to any one of the foregoing information carrier claims.~~

12. (Currently Amended) A recording device for writing patterns ~~which represent~~^{representing} user information onto an information carrier which comprises a predetermined synchronization pattern ~~as claimed in claim 8~~, the recording device comprising reading means 5 for reading the predetermined synchronization pattern, synchronization means for setting a clock frequency and for setting a dynamic range of an amplifier in response to the predetermined synchronization pattern read, characterized in that the synchronization means comprise means for setting the clock frequency and for setting the dynamic range of an amplifier in 10 response to the predetermined synchronization pattern ~~according to any one of the foregoing information carrier claims.~~

13. (New) The information carrier as claimed in claim 8, characterized in that the total length of all the marks in the predetermined synchronization pattern is substantially equal to the total length of all the spaces in the predetermined synchronization 5 pattern.

14. (New) An information carrier comprising a synchronization area, said synchronization area comprising a predetermined synchronization pattern for synchronizing a clock frequency in a device in which the information carrier is used,

5 characterized in that the predetermined synchronization pattern comprises a first part and a second part, the second part being distinguishable from the first part, characterized in that the predetermined synchronization pattern is composed of marks and of spaces between the marks, and in that the first part of the

10 predetermined synchronization pattern contains marks having a first length and spaces having a second length whereas the second part of the synchronization pattern contains marks having a third length and spaces having a fourth length, the first length being different from the third length and the second length being different from

15 the fourth length, in that the header information is converted into patterns in the header area according to a (d, k) Run Length Limited modulation code, in which d represents a predetermined natural number larger than zero and k represents a predetermined natural number larger than d , and the length of each mark and each space

20 expressed as a number of channel bit lengths (T), and in that the first part of the predetermined synchronization pattern contains marks having a first length of $(d+1)$ times the channel bit length, and spaces having a second length of $(d+1)$ times the channel bit

length, and the second part of the predetermined synchronization
25 pattern contains marks having a third length of $(k+1)$ times the
channel bit length and spaces having a fourth length of $(k+1)$ times
the channel bit length, and in that the predetermined
synchronization pattern also comprises a third part, which third
part contains marks having a length of $(k-d)$ times the channel bit
30 length and spaces also having a length of $(k-d)$ times the channel
bit length.

15. (New) The information carrier as claimed in claim 14,
characterized in that the total length of all the marks in the
predetermined synchronization pattern is substantially equal to the
total length of all the spaces in the predetermined synchronization
5 pattern.